

SuperFET\*\*

# FCP20N60 / FCPF20N60 600V N-Channel MOSFET

#### **Features**

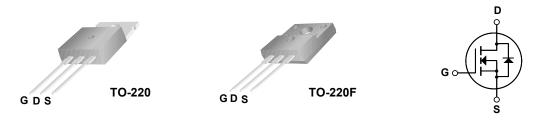
- 650V @T<sub>.I</sub> = 150°C
- Typ.  $R_{DS(on)} = 0.15\Omega$
- Ultra low gate charge (typ. Q<sub>g</sub> = 75nC)
- Low effective output capacitance (typ. Coss.eff = 165pF)
- 100% avalanche tested
- · RoHS Compliant



### **Description**

SuperFET<sup>TM</sup> is, Fairchild's proprietary, new generation of high voltage MOSFET family that is utilizing an advanced charge balance mechanism for outstanding low on-resistance and lower gate charge performance.

This advanced technology has been tailored to minimize conduction loss, provide superior switching performance, and withstand extreme dv/dt rate and higher avalanche energy. Consequently, SuperFET is very suitable for various AC/DC power conversion in switching mode operation for system miniaturization and higher efficiency.



# **Absolute Maximum Ratings**

Symbol	Parameter		FCP20N60 FCPF20N60		Unit	
V <sub>DSS</sub>	Drain-Source Voltage		600		V	
I <sub>D</sub>	Drain Current - Continuous (T <sub>C</sub> = 25°C) - Continuous (T <sub>C</sub> = 100°C)		20 20* 12.5 12.5*		A A	
I <sub>DM</sub>	Drain Current	- Pulsed	(Note 1)	60	60*	Α
V <sub>GSS</sub>	Gate-Source voltage		± 30		V	
E <sub>AS</sub>	Single Pulsed Avalanche Energy (Note 2)		(Note 2)	690		mJ
I <sub>AR</sub>	Avalanche Current (N		(Note 1)	20		Α
E <sub>AR</sub>	Repetitive Avalanche Energy (Note 1)		(Note 1)	20.8		mJ
dv/dt	Peak Diode Recovery dv/dt (Note 3)		4.5		V/ns	
P <sub>D</sub>	Power Dissipation (T <sub>C</sub> = 25°C) - Derate above 25°C		208 1.67	39 0.3	W W/°C	
T <sub>J</sub> , T <sub>STG</sub>	Operating and Storage Temperature Range		-55 to +150		°C	
T <sub>L</sub>	Maximum Lead Temperature for Soldering Purpose, 1/8" from Case for 5 Seconds		300		°C	

<sup>\*</sup>Drain current limited by maximum junction temperature

#### **Thermal Characteristics**

Symbol	Parameter	FCP20N60	FCPF20N60	Unit
$R_{\theta JC}$	Thermal Resistance, Junction-to-Case	0.6	3.2	°C/W
$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient	62.5	62.5	°C/W

# **Package Marking and Ordering Information**

Device Marking	Device	Package	Reel Size	Tape Width	Quantity
FCP20N60	FCP20N60	TO-220	-	-	50
FCPF20N60	FCPF20N60	TO-220F	-	-	50

# **Electrical Characteristics** $T_C = 25$ °C unless otherwise noted

Symbol	Parameter	Conditions	Min	Тур	Max	Units
Off Charac	teristics					1
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage	$V_{GS} = 0V$ , $I_D = 250\mu A$ , $T_J = 25^{\circ}C$	600			V
		V <sub>GS</sub> = 0V, I <sub>D</sub> = 250μA, T <sub>J</sub> = 150°C		650		V
ΔBV <sub>DSS</sub> / ΔT <sub>J</sub>	Breakdown Voltage Temperature Coefficient	I <sub>D</sub> = 250μA, Referenced to 25°C		0.6		V/°C
BV <sub>DS</sub>	Drain-Source Avalanche Breakdown Voltage	V <sub>GS</sub> = 0V, I <sub>D</sub> = 20A		700		٧
I <sub>DSS</sub>	Zero Gate Voltage Drain Current	V <sub>DS</sub> = 600V, V <sub>GS</sub> = 0V V <sub>DS</sub> = 480V, T <sub>C</sub> = 125°C			1 10	μ <b>Α</b> μ <b>Α</b>
I <sub>GSSF</sub>	Gate-Body Leakage Current, Forward	V <sub>GS</sub> = 30V, V <sub>DS</sub> = 0V			100	nA
I <sub>GSSR</sub>	Gate-Body Leakage Current, Reverse	V <sub>GS</sub> = -30V, V <sub>DS</sub> = 0V	-		-100	nA
On Charac	teristics					
V <sub>GS(th)</sub>	Gate Threshold Voltage	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	3.0		5.0	V
R <sub>DS(on)</sub>	Static Drain-Source On-Resistance	V <sub>GS</sub> = 10V, I <sub>D</sub> = 10A		0.15	0.19	Ω
9 <sub>FS</sub>	Forward Transconductance	V <sub>DS</sub> = 40V, I <sub>D</sub> = 10A (Note 4)		17		S
Dynamic C	Characteristics					
C <sub>iss</sub>	nput Capacitance $V_{DS} = 25V, V_{GS} = 0V,$			2370	3080	pF
C <sub>oss</sub>	Output Capacitance	f = 1.0MHz	-	1280	1665	pF
C <sub>rss</sub>	Reverse Transfer Capacitance			95		pF
C <sub>oss</sub>	Output Capacitance	V <sub>DS</sub> = 480V, V <sub>GS</sub> = 0V, f = 1.0MHz		65	85	pF
Coss eff.	Effective Output Capacitance	$V_{DS}$ = 0V to 400V, $V_{GS}$ = 0V	1	165		pF
Switching	Characteristics					
t <sub>d(on)</sub>	Turn-On Delay Time			62	135	ns
t <sub>r</sub>	Turn-On Rise Time	$R_{G} = 25\Omega$		140	290	ns
t <sub>d(off)</sub>	Turn-Off Delay Time			230	470	ns
t <sub>f</sub>	Turn-Off Fall Time	(Note 4, 5)	-	65	140	ns
Qg	Total Gate Charge	V <sub>DS</sub> = 480V, I <sub>D</sub> = 20A		75	98	nC
$Q_{gs}$	Gate-Source Charge	V <sub>GS</sub> = 10V		13.5	18	nC
Q <sub>gd</sub>	Gate-Drain Charge	(Note 4, 5)	-	36		nC
Drain-Sour	rce Diode Characteristics and Maximur	n Ratings				
S Maximum Continuous Drain-Source Diode Forward Current					20	Α
I <sub>SM</sub>	Maximum Pulsed Drain-Source Diode F	orward Current			60	Α
$V_{SD}$	Drain-Source Diode Forward Voltage	V <sub>GS</sub> = 0V, I <sub>S</sub> = 20A			1.4	٧
t <sub>rr</sub>	Reverse Recovery Time	V <sub>GS</sub> = 0V, I <sub>S</sub> = 20A	-	530		ns
Q <sub>rr</sub>	Reverse Recovery Charge	$dI_F/dt = 100A/\mu s$ (Note 4)		10.5		μС

#### Notes

<sup>1.</sup> Repetitive Rating: Pulse width limited by maximum junction temperature

<sup>2.</sup> I $_{AS}$  = 10A, V $_{DD}$  = 50V, R $_{G}$  = 25 $\Omega$ , Starting T $_{J}$  = 25 $^{\circ}$ C

<sup>3.</sup> I $_{SD}$   $\leq$  20A, di/dt  $\leq$  200A/ $\mu$ s, V $_{DD}$   $\leq$  BV $_{DSS}$ , Starting T $_{J}$  = 25°C

<sup>4.</sup> Pulse Test: Pulse width  $\leq 300 \mu s, \, \text{Duty Cycle} \leq 2\%$ 

<sup>5.</sup> Essentially Independent of Operating Temperature Typical Characteristics

# **Typical Performance Characteristics**

Figure 1. On-Region Characteristics

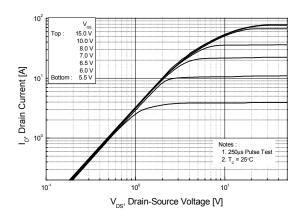


Figure 3. On-Resistance Variation vs.

Drain Current and Gate Voltage

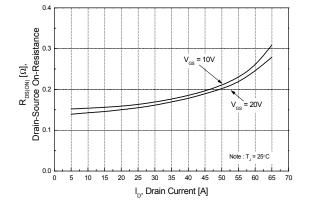


Figure 2. Transfer Characteristics

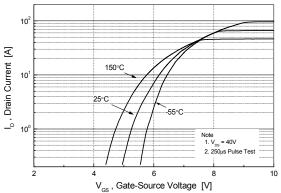
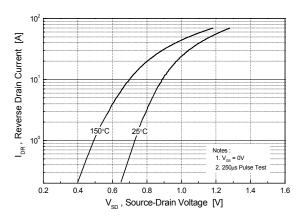


Figure 4. Body Diode Forward Voltage Variation vs. Source Current and Temperatue



**Figure 5. Capacitance Characteristics** 

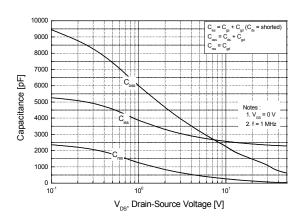
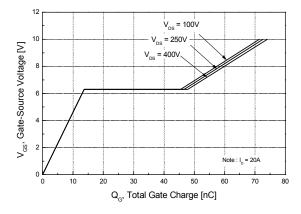


Figure 6. Gate Charge Characteristics



# **Typical Performance Characteristics** (Continued)

Figure 7. Breakdown Voltage Variation vs. Temperature

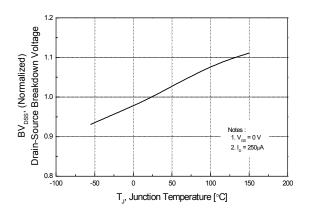


Figure 8. On-Resistance Variation vs. Temperature

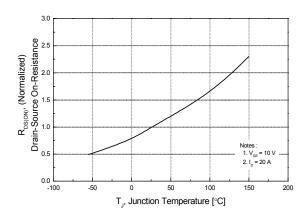


Figure 9-1. Maximum Safe Operating Area for FCP20N60

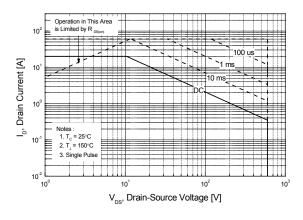


Figure 9-2. Maximum Safe Operating Area for FCPF20N60

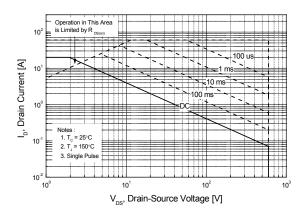
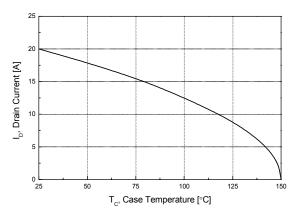


Figure 10. Maximum Drain Current vs. Case Temperature



4

# **Typical Performance Characteristics** (Continued)

Figure 11-1. Transient Thermal Response Curve for FCP20N60

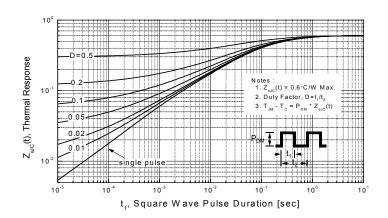
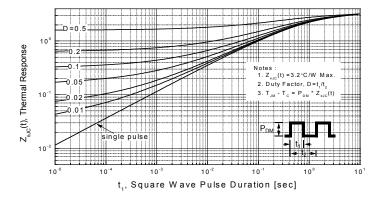
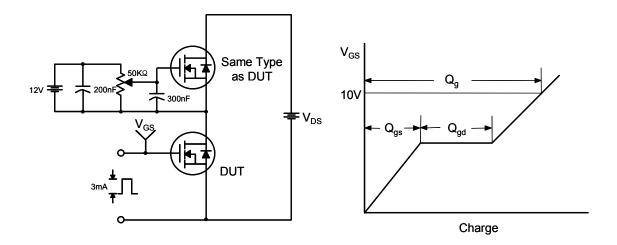


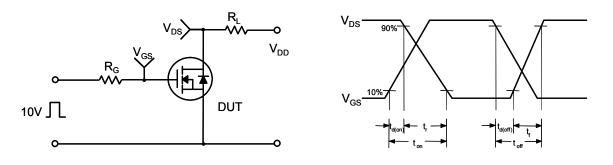
Figure 11-2. Transient Thermal Response Curve for FCPF20N60



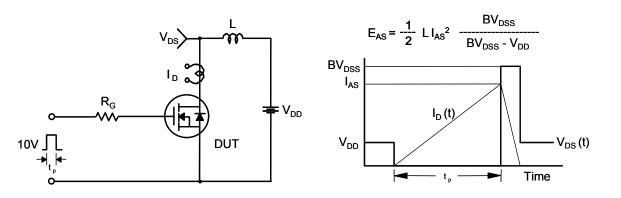
### **Gate Charge Test Circuit & Waveform**



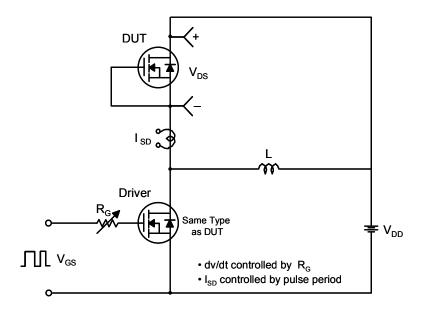
#### **Resistive Switching Test Circuit & Waveforms**

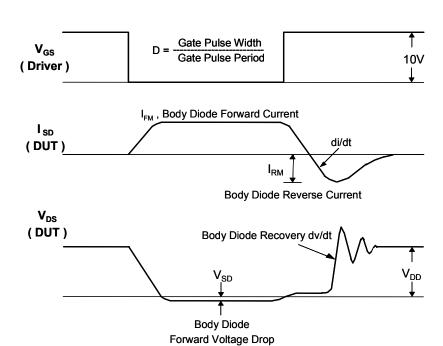


#### **Unclamped Inductive Switching Test Circuit & Waveforms**



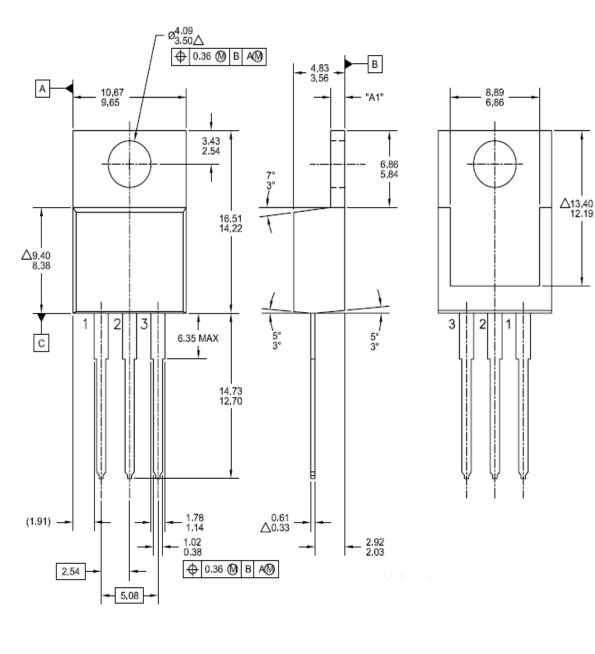
#### Peak Diode Recovery dv/dt Test Circuit & Waveforms

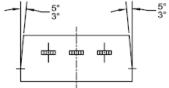




## **Mechanical Dimensions**

TO - 220





Dimensions in Millimeters

# Mechanical Dimensions (Continued) TO-220F $3.30 \pm 0.10$ 10.1<u>6 ±0.20</u> $2.54 \pm 0.20$ $\emptyset 3.18 \pm 0.10$ (7.00)(0.70) $6.68 \pm 0.20$ Ф $15.87 \pm 0.20$ $15.80 \pm 0.20$ (1.00x45°) MAX1.47 $9.75 \pm 0.30$ $0.80 \pm 0.10$ $0.35 \pm 0.10$ $0.50^{\,+0.10}_{\,-0.05}$ $2.76 \pm 0.20$ 2.54TYP 2.54TYP [2.54 ±0.20] [2.54 ±0.20] $4.70 \pm 0.20$

 $9.40 \pm 0.20$ 

Dimensions in Millimeters





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