

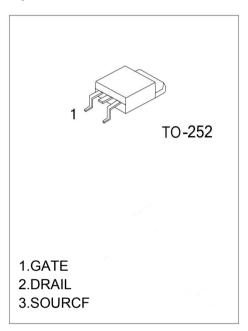
# N-Channel 60-V(D-S) Power MOSFET

V(BR)DSS	RDS(on)MAX	ID
60 V	35mΩ@ 10 V	30A
60 V	45mΩ@ 4.5 V	30A

### **General Description:**

The high voltage MOSFET uses an advanced termination scheme to provide enhanced voltage-blocking capability without degrading performance over time. In addition , this advanced MOSFET is designed to withstand high energy in avalanche and commutation modes . The new energy efficient design also offers a drain-to-source diode with a fast recovery time. Designed for high voltage, high speed switching applications in power suppliers, converters and PWM motor controls , these devices are particularly well suited for bridge circuits where diode speed and commutating safe operating areas are critical and offer additional and safety margin against unexpected voltage transients.

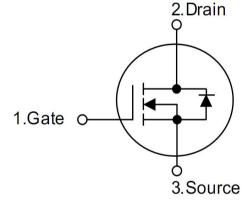
#### **Equivalent Circuit:**



#### **FEATURE:**

- Power switching application
- Hard switched and high frequency circuits
- ※ Uninterruptible power supply
- Fully characterized avalanche voltage and current
- ※ Excellent package for good heat dissipation
- Good stability and uniformity with high EAS

## SYMBOL:



### Maximum ratings ( Ta=25 $^{\circ}$ C unless otherwise noted)

Parameter	Symbol	Value	Unit	
Drain-Source Voltage	VDS	60	V	
Gate-Source Voltage	VGS	±20	v	
Continuous Drain Current	ID	30	^	
Pulsed Diode Curren	IDM	120	4 A	
Power Dissipation	PD	46	W	
Thermal Resistance from Junction to Ambient (t≤10s)	RθJA	100	°C/W	
Operating Junction	TJ	150	°C	
Storage Temperature	TSTG	-55~+150	°C	



#### **MOSFET ELECTRICAL CHARACTERISTICS**

Static Electrical Characteristics (Ta = 25 ℃ Unless Otherwise Noted)

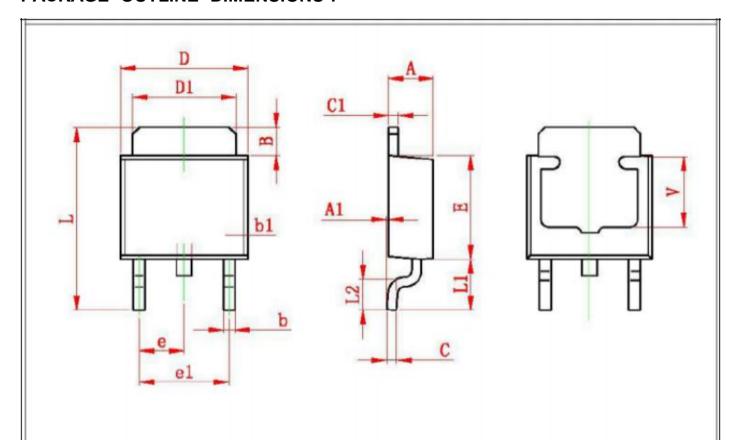
Parameter	Symbol	Test Condition	Min	Тур	Max	Unit
Static	•					
Drain-source breakdown voltage	V(BR)DSS	VGS = 0V, ID = 250μA	60			V
Gate-source threshold voltage	VGS(th)	VDS =VGS, ID = 250μA	1		2.5	V
Gate-source leakage	IGSS	VDS =0V, VGS = ±20V			±100	nA
Zero gate voltage drain current	IDSS	VDS = 60V, VGS =0V			10	μA
Duning and the second	RDS(on)	VGS = 10V, ID = 16A			35	mΩ
Drain-source on-state resistancea	RDS(on)	VGS = 4.5V, ID = 8A			45	mΩ
Forward transconductancea	gfs	VDS = 25V, ID = 15A		23		S
Diode forward voltage	VSD	IS= 30A, VGS=0V		0.8	1.5	V
Dynamic	•				•	•
Input capacitance	Ciss			900		pF
Output capacitance	Coss	VDS = 25V, VGS =0V, f=1MHz		250		pF
Reverse transfer capacitanceb	Crss			85		pF
Total gate charge	Qg			20		nC
Gate-source charge	Qgs	VDS = 60V, VGS = 10V, ID = 15A		6		nC
Gate-drain charge	Qgd	15 10/1		12		nC
Switchingb	•					
Turn-on delay time	td(on)			50		ns
Rise time	tr	VDD= 30V - RL= 18Ω, ID = 15A, VGEN= 10V,Rg= 18Ω		100		ns
Turn-off delay time	td(off)			160		ns
Fall time	tf			10		ns
Drain-Source Diode Characteris	stics					
Continuous Source-Drain Diode Current	IS				30	Α
Pulsed Diode forward Curren	ISM				120	Α

#### Note:

- 1. Repetitive Rating: Pulse width limited by maximum junction temperature.
- 2. Surface Mounted on FR4 Board, t < 10 sec.
- 3. Pulse Test : Pulse Width≤300µs, Duty Cycle ≤ 2%.
- 4. Guaranteed by design, not subject to production testing.



# PACKAGE OUTLINE DIMENSIONS:



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
Α	2.200	2.400	0.087	0.094
A1	0.000	0.127	0.000	0.005
В	1.350	1.650	0.053	0.065
b	0.500	0.700	0.020	0.028
b1	0.700	0.900	0.028	0.035
С	0.430	0.580	0.017	0.023
c1	0.430	0.580	0.017	0.023
D	6.350	6.650	0.250	0.262
D1	5.200	5.400	0.205	0.213
E	5.400	5.700	0.213	0.224
е	2.300 TYP		0.091 TYP	
e1	4.500	4.700	0.177	0.185
L	9.500	9.900	0.374	0.390
L1	2.550	2.900	0.100	0.114
L2	1.400	1.780	0.055	0.070
V	3.80 REF		0.150 REF	