

## FEATURES

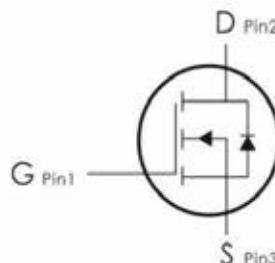
- Fast switching
- 100% avalanche tested
- Improved dv/dt capability

## APPLICATIONS

- Switch Mode Power Supply (SMPS)
- Uninterruptible Power Supply (UPS)
- Power Factor Correction (PFC)
- High speed switching
- DC-DC Converters
- Motor controls



TO-220-3L



### Device Marking and Package Information

Device	Package	Marking
STP60NF06FP	TO-220	P60NF06FP

### Absolute Maximum Ratings $T_C = 25^\circ\text{C}$ , unless otherwise noted

Parameter	Symbol	Value	Unit
		TO-220	
Drain-Source Voltage ( $V_{GS} = 0\text{V}$ )	$V_{DSS}$	60	V
Continuous Drain Current	$I_D$	60	A
Pulsed Drain Current (note1)	$I_{DM}$	240	A
Gate-Source Voltage	$V_{GSS}$	$\pm 20$	V
Single Pulse Avalanche Energy (note2)	$E_{AS}$	250	mJ
Avalanche Current (note1)	$I_{AR}$	50	A
Repetitive Avalanche Energy (note1)	$E_{AR}$	36	mJ
Power Dissipation ( $T_C = 25^\circ\text{C}$ )	$P_D$	110	W
Operating Junction and Storage Temperature Range	$T_J, T_{stg}$	-55~+150	$^\circ\text{C}$

### Thermal Resistance

Parameter	Symbol	Value	Unit
		TO-220	
Thermal Resistance, Junction-to-Case	$R_{thJC}$	1.14	K/W
Thermal Resistance, Junction-to-Ambient	$R_{thJA}$	60	

**Specifications  $T_J = 25^\circ\text{C}$ , unless otherwise noted**

Parameter	Symbol	Test Conditions	Value			Unit
			Min.	Typ.	Max.	
<b>Static</b>						
Drain-Source Breakdown Voltage	$V_{(\text{BR})\text{DSS}}$	$V_{GS} = 0\text{V}, I_D = 250\mu\text{A}$	60	--	--	V
Zero Gate Voltage Drain Current	$I_{\text{DSS}}$	$V_{DS} = 60\text{V}, V_{GS} = 0\text{V}, T_J = 25^\circ\text{C}$	--	--	5	$\mu\text{A}$
		$V_{DS} = 48\text{V}, V_{GS} = 0\text{V}, T_J = 125^\circ\text{C}$	--	--	100	
Gate-Source Leakage	$I_{GSS}$	$V_{GS} = \pm 20\text{V}$	--	--	$\pm 100$	nA
Gate-Source Threshold Voltage	$V_{GS(\text{th})}$	$V_{DS} = V_{GS}, I_D = 250\mu\text{A}$	2.0	--	4.0	V
Drain-Source On-Resistance (Note3)	$R_{DS(\text{on})}$	$V_{GS} = 10\text{V}, I_D = 25\text{A}$	--	14	22	$\text{m}\Omega$
<b>Dynamic</b>						
Input Capacitance	$C_{iss}$	$V_{GS} = 0\text{V}, V_{DS} = 25\text{V}, f = 1.0\text{MHz}$	--	1489	--	$\text{pF}$
Output Capacitance	$C_{oss}$		--	608	--	
Reverse Transfer Capacitance	$C_{rss}$		--	275	--	
Total Gate Charge	$Q_g$	$V_{DD} = 48\text{V}, I_D = 50\text{A}, V_{GS} = 10\text{V}$	--	60	--	$\text{nC}$
Gate-Source Charge	$Q_{gs}$		--	6	--	
Gate-Drain Charge	$Q_{gd}$		--	31	--	
Turn-on Delay Time	$t_{d(on)}$	$V_{DD} = 30\text{V}, I_D = 50\text{A}, R_G = 25\Omega$	--	22	--	$\text{ns}$
Turn-on Rise Time	$t_r$		--	82	--	
Turn-off Delay Time	$t_{d(off)}$		--	52	--	
Turn-off Fall Time	$t_f$		--	93	--	
<b>Drain-Source Body Diode Characteristics</b>						
Continuous Body Diode Current	$I_S$	$T_C = 25^\circ\text{C}$	--	--	60	$\text{A}$
Pulsed Diode Forward Current	$I_{SM}$		--	--	240	
Body Diode Voltage	$V_{SD}$	$T_J = 25^\circ\text{C}, I_{SD} = 25\text{A}, V_{GS} = 0\text{V}$	--	--	2	V
Reverse Recovery Time	$t_{rr}$	$V_{GS} = 0\text{V}, I_S = 50\text{A}, dI/dt = 100\text{A}/\mu\text{s}$	--	68	--	$\text{ns}$
Reverse Recovery Charge	$Q_{rr}$		--	4.2	--	

**Notes**

- Repetitive Rating: Pulse width limited by maximum junction temperature
- $L=1\text{mH}, V_{DD} = 50\text{V}, R_G = 25\Omega$ , Starting  $T_J = 25^\circ\text{C}$
- Pulse Test: Pulse width  $\leq 300\mu\text{s}$ , Duty Cycle  $\leq 1\%$

**Typical Characteristics**  $T_J = 25^\circ\text{C}$ , unless otherwise noted

Figure 1. Output Characteristics ( $T_J = 25^\circ\text{C}$ )

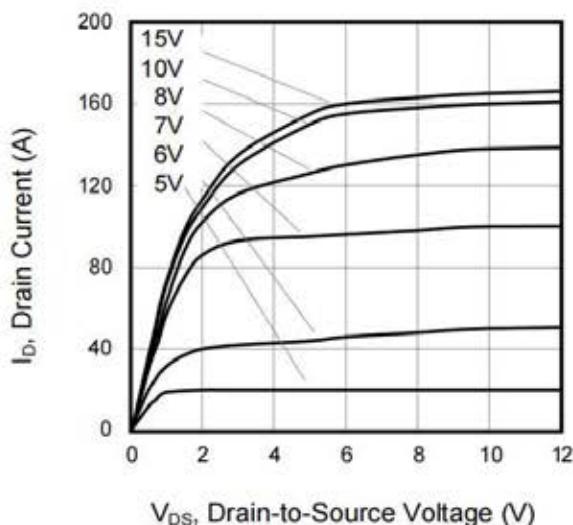


Figure 2. Body Diode Forward Voltage

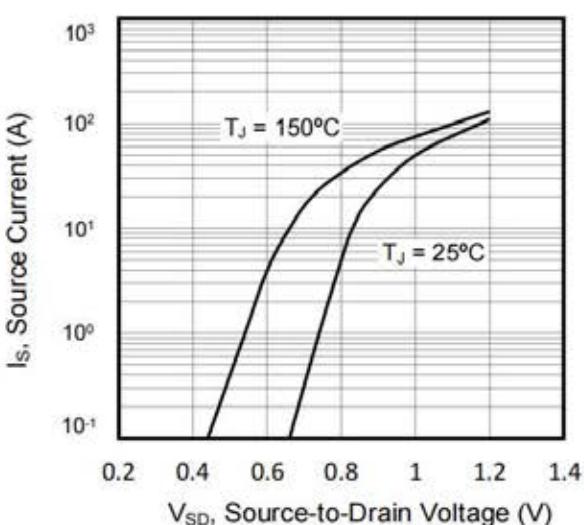


Figure 3. Drain Current vs. Temperature

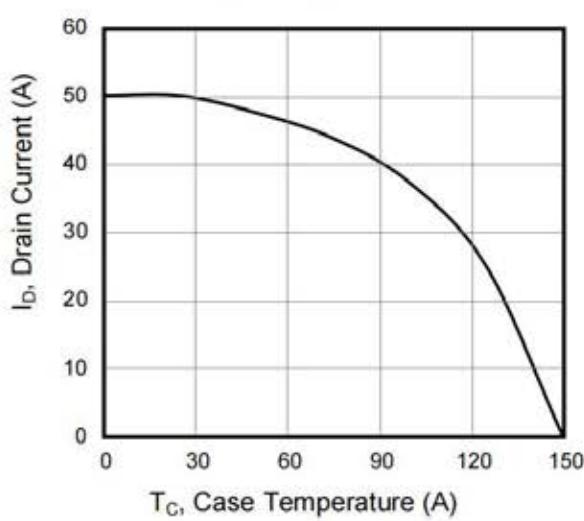


Figure 4.  $BV_{DSS}$  Variation vs. Temperature

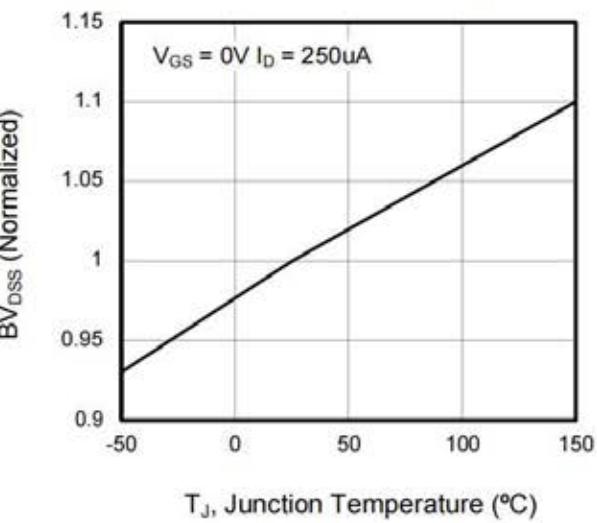


Figure 5. Transfer Characteristics

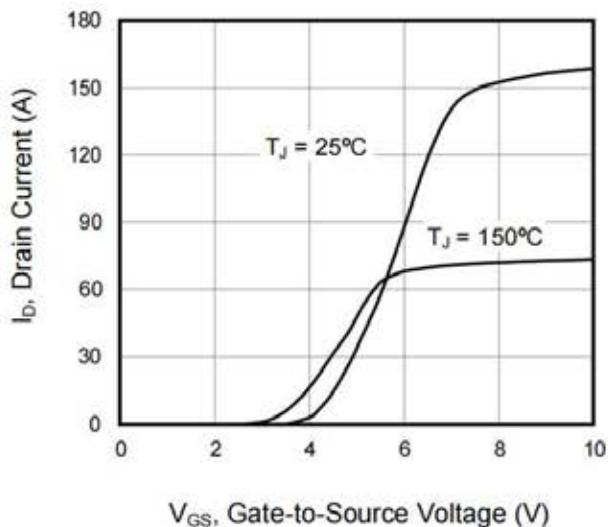
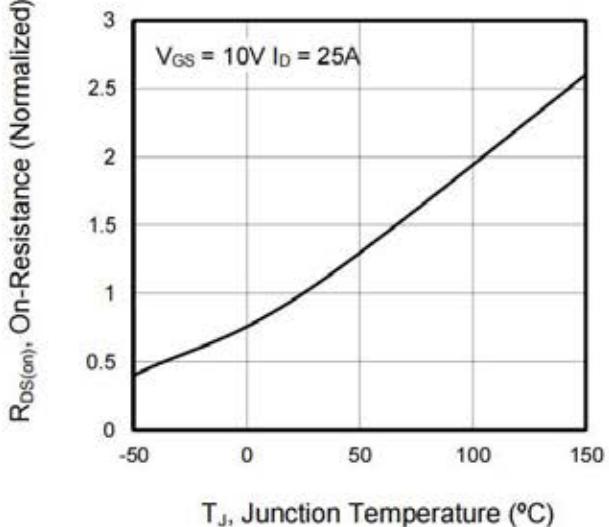
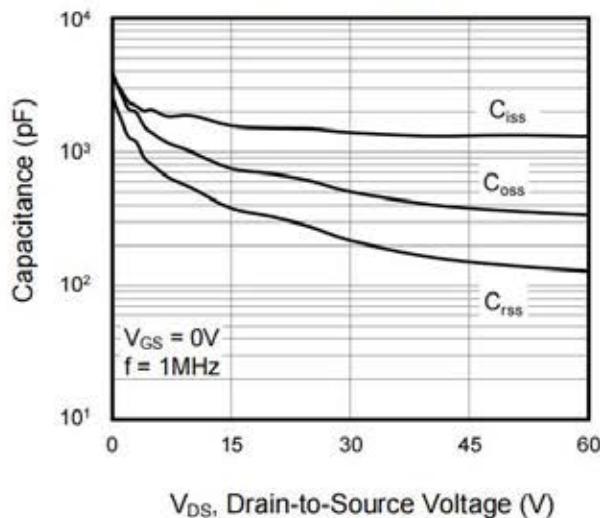


Figure 6. On-Resistance vs. Temperature

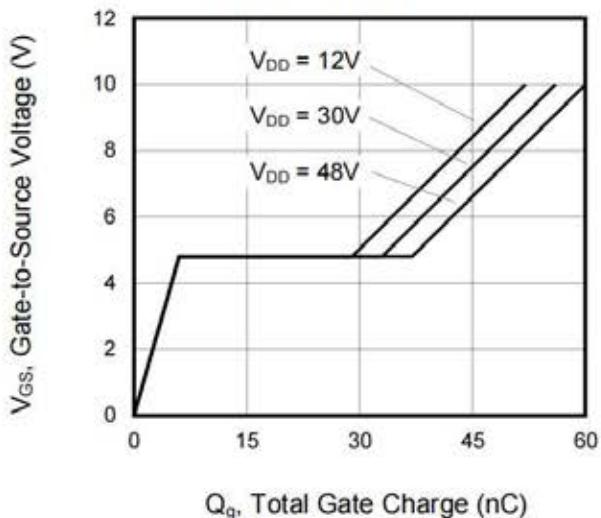


**Typical Characteristics**  $T_J = 25^\circ\text{C}$ , unless otherwise noted

**Figure 7. Capacitance**



**Figure 8. Gate Charge**



**Figure 9. Transient Thermal Impedance**  
**TO-220**

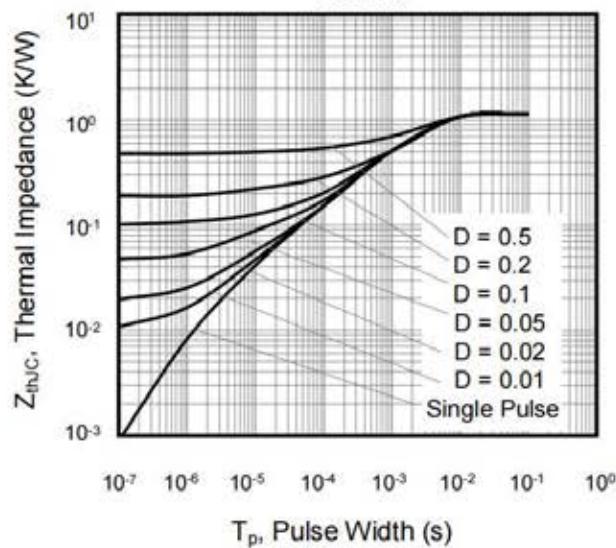


Figure A: Gate Charge Test Circuit and Waveform

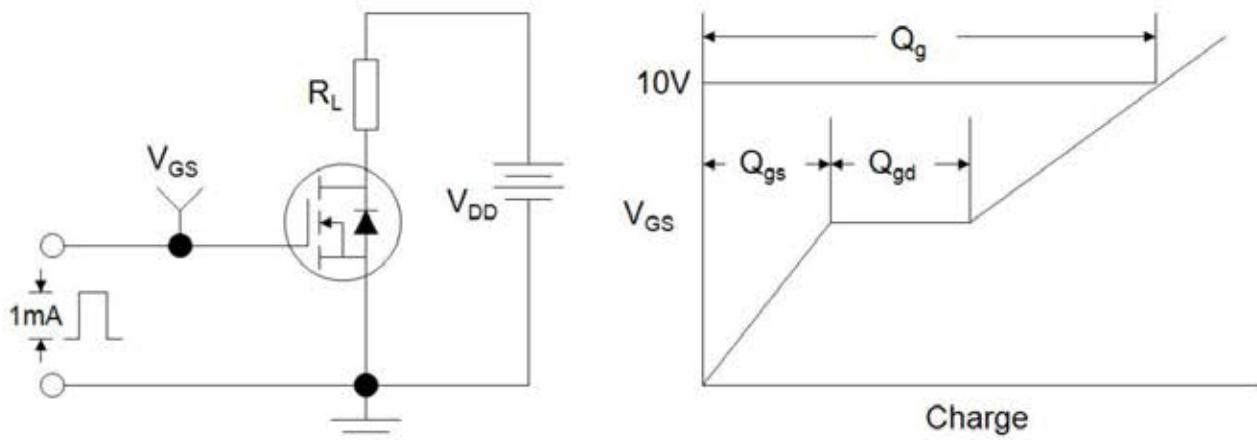


Figure B: Resistive Switching Test Circuit and Waveform

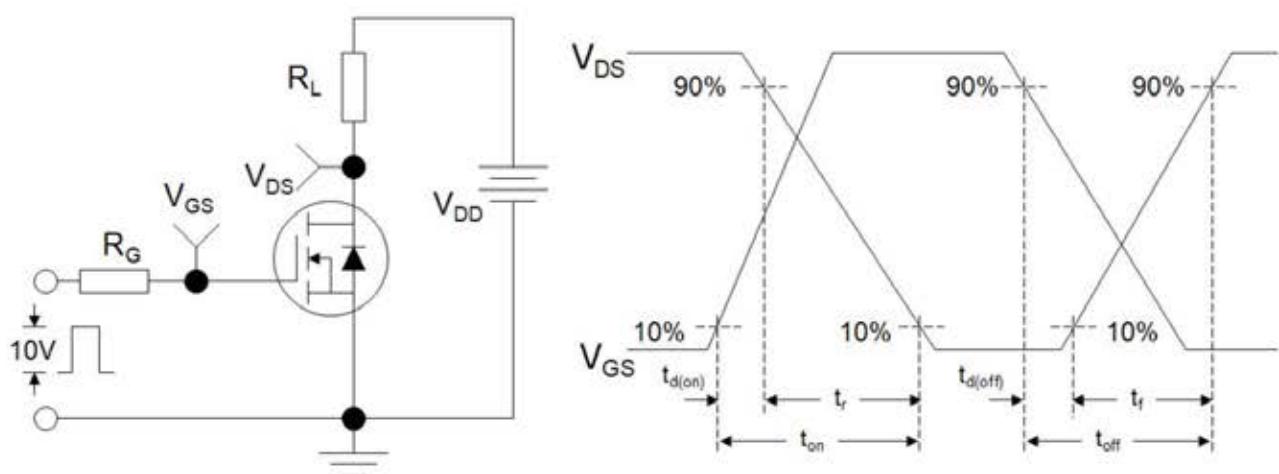
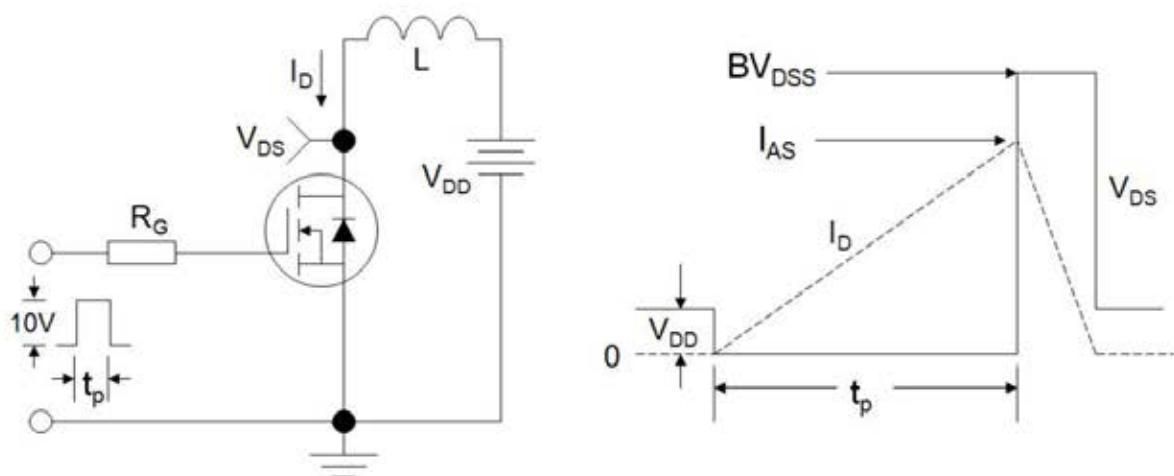
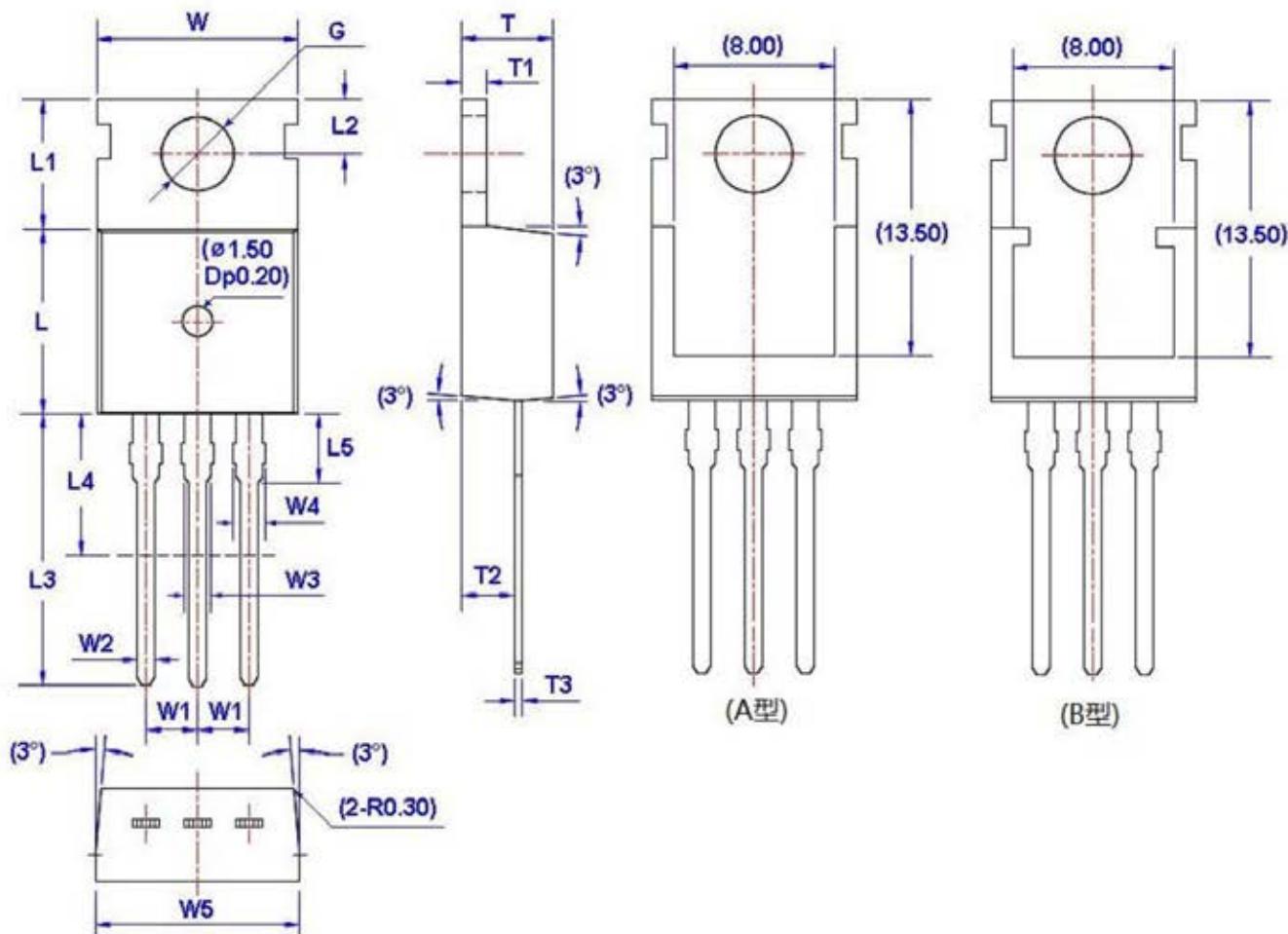


Figure C: Unclamped Inductive Switching Test Circuit and Waveform



## Package Outline: TO-220



Unit: mm

Symbol	Size		Symbol	Size		Symbol	Size		Symbol	Size	
	Min	Max		Min	Max		Min	Max		Min	Max
W	9.66	10.28	W5	9.80	10.20	L4**	6.20	6.60	T3	0.45	0.60
W1	2.54 (TYP)		L	9.00	9.40	L5	2.79	3.30	G(Φ)	3.50	3.70
W2	0.70	0.95	L1	6.40	6.80	T	4.30	4.70			
W3	1.17	1.37	L2	2.70	2.90	T1	1.15	1.40			
W4*	1.32	1.72	L3	12.70	14.27	T2	2.20	2.60			