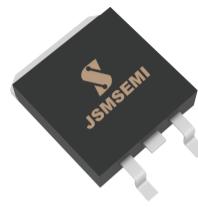


## FEATURES

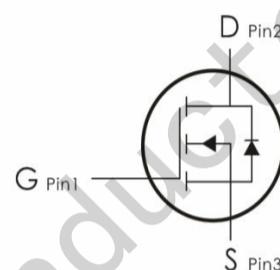
- Fast switching
- 100% avalanche tested
- Improved dv/dt capability



TO-263-2L

## APPLICATIONS

- Switch Mode Power Supply (SMPS)
- Uninterruptible Power Supply (UPS)
- Power Factor Correction (PFC)



Device Marking and Package Information		
Device	Package	Marking
IRF740ASTRLPBF	TO-263	IRF740AS

## Absolute Maximum Ratings $T_C = 25^\circ\text{C}$ , unless otherwise noted

Parameter	Symbol	Value	Unit
		TO-263	
Drain-Source Voltage ( $V_{GS} = 0\text{V}$ )	$V_{DSS}$	400	V
Continuous Drain Current	$I_D$	10	A
Pulsed Drain Current (note1)	$I_{DM}$	40	A
Gate-Source Voltage	$V_{GSS}$	$\pm 30$	V
Single Pulse Avalanche Energy (note2)	$E_{AS}$	96	mJ
Avalanche Current (note1)	$I_{AS}$	4.4	A
Repetitive Avalanche Energy (note1)	$E_{AR}$	57	mJ
Power Dissipation ( $T_C = 25^\circ\text{C}$ )	$P_D$	63	W
Operating Junction and Storage Temperature Range	$T_J, T_{stg}$	-55~+150	°C

## Thermal Resistance

Parameter	Symbol	Value	Unit
		TO-263	
Thermal Resistance, Junction-to-Case	$R_{thJC}$	1.5	
Thermal Resistance, Junction-to-Ambient	$R_{thJA}$	60	K/W

**Specifications  $T_J = 25^\circ\text{C}$ , unless otherwise noted**

Parameter	Symbol	Test Conditions	Value			Unit
			Min.	Typ.	Max.	
<b>Static</b>						
Drain-Source Breakdown Voltage	$V_{(\text{BR})\text{DSS}}$	$V_{\text{GS}} = 0\text{V}, I_D = 250\mu\text{A}$	400	--	--	V
Zero Gate Voltage Drain Current	$I_{\text{DSS}}$	$V_{\text{DS}} = 400\text{V}, V_{\text{GS}} = 0\text{V}, T_J = 25^\circ\text{C}$	--	--	1	$\mu\text{A}$
Gate-Source Leakage	$I_{\text{GSS}}$	$V_{\text{GS}} = \pm 30\text{V}$	--	--	$\pm 100$	nA
Gate-Source Threshold Voltage	$V_{\text{GS}(\text{th})}$	$V_{\text{DS}} = V_{\text{GS}}, I_D = 250\mu\text{A}$	3.0	--	4.0	V
Drain-Source On-Resistance (Note3)	$R_{\text{DS}(\text{on})}$	$V_{\text{GS}} = 10\text{V}, I_D = 5\text{A}$	--	0.45	0.55	$\Omega$
<b>Dynamic</b>						
Input Capacitance	$C_{\text{iss}}$	$V_{\text{GS}} = 0\text{V}, V_{\text{DS}} = 25\text{V}, f = 1.0\text{MHz}$	--	1050	--	pF
Output Capacitance	$C_{\text{oss}}$		--	112	--	
Reverse Transfer Capacitance	$C_{\text{rss}}$		--	8	--	
Total Gate Charge	$Q_g$	$V_{\text{DD}} = 320\text{V}, I_D = 10\text{A}, V_{\text{GS}} = 10\text{V}$	--	28	--	nC
Gate-Source Charge	$Q_{\text{gs}}$		--	9	--	
Gate-Drain Charge	$Q_{\text{gd}}$		--	7	--	
Turn-on Delay Time	$t_{\text{d}(\text{on})}$	$V_{\text{DD}} = 200\text{V}, I_D = 10\text{A}, R_G = 25\Omega$	--	39	--	ns
Turn-on Rise Time	$t_r$		--	20	--	
Turn-off Delay Time	$t_{\text{d}(\text{off})}$		--	138	--	
Turn-off Fall Time	$t_f$		--	34	--	
<b>Drain-Source Body Diode Characteristics</b>						
Continuous Body Diode Current	$I_s$	$T_C = 25^\circ\text{C}$	--	--	10	A
Pulsed Diode Forward Current	$I_{\text{SM}}$		--	--	40	
Body Diode Voltage	$V_{\text{SD}}$	$T_J = 25^\circ\text{C}, I_{\text{SD}} = 5\text{A}, V_{\text{GS}} = 0\text{V}$	--	--	1.4	V
Reverse Recovery Time	$t_{\text{rr}}$	$V_{\text{GS}} = 0\text{V}, I_s = 10\text{A}, dI/dt = 100\text{A}/\mu\text{s}$	--	249	--	ns
Reverse Recovery Charge	$Q_{\text{rr}}$		--	1.87	--	$\mu\text{C}$

**Notes**

1. Repetitive Rating: Pulse width limited by maximum junction temperature
2. L = 10.0mH,  $V_{\text{DD}} = 50\text{V}$ ,  $R_G = 25\Omega$ , Starting  $T_J = 25^\circ\text{C}$
3. Pulse Test: Pulse width  $\leq 300\mu\text{s}$ , Duty Cycle  $\leq 1\%$

Typical Characteristics  $T_J = 25^\circ\text{C}$ , unless otherwise noted

Figure 1. Output Characteristics ( $T_J = 25^\circ\text{C}$ )

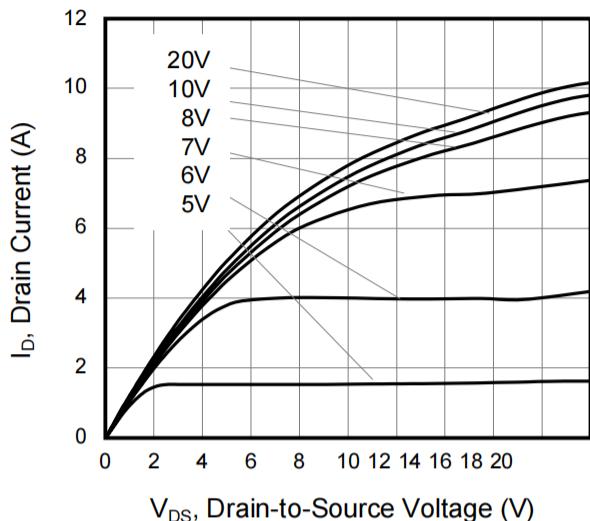


Figure 2. Body Diode Forward Voltage

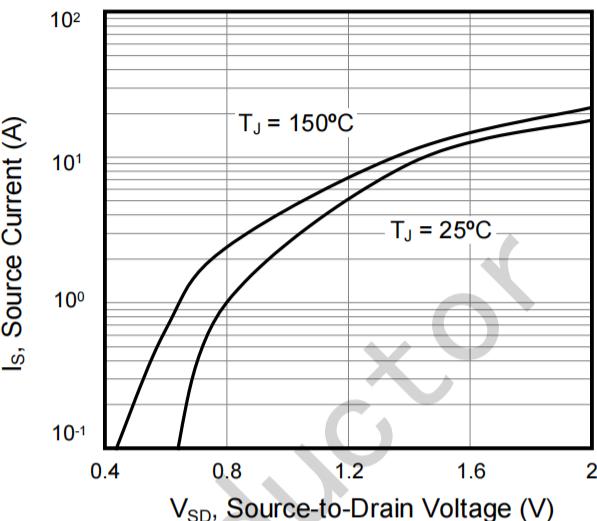


Figure 3. Drain Current vs. Temperature

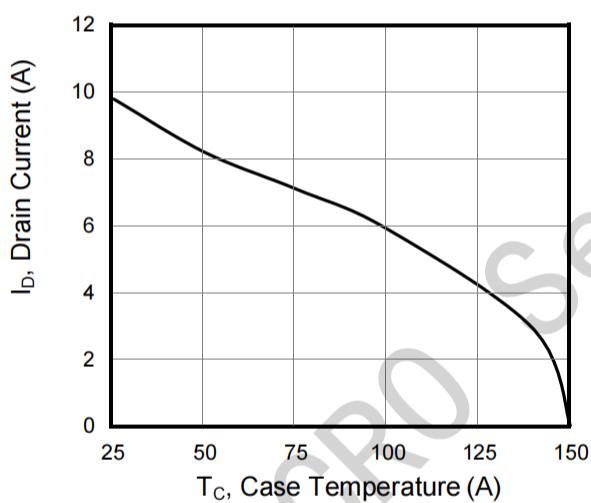


Figure 4.  $BV_{DSS}$  Variation vs. Temperature

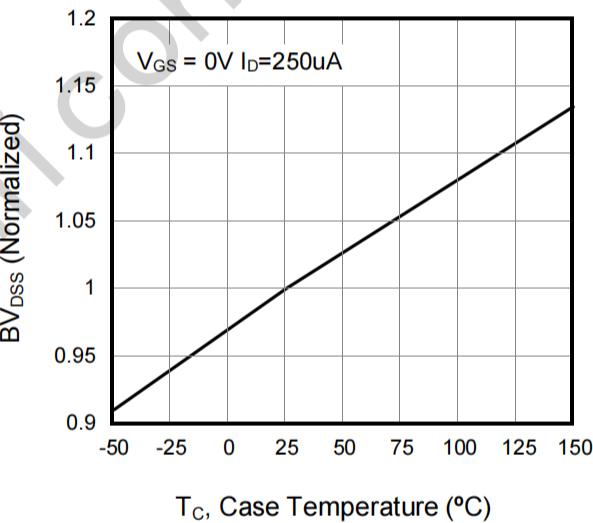


Figure 5. Transfer Characteristics

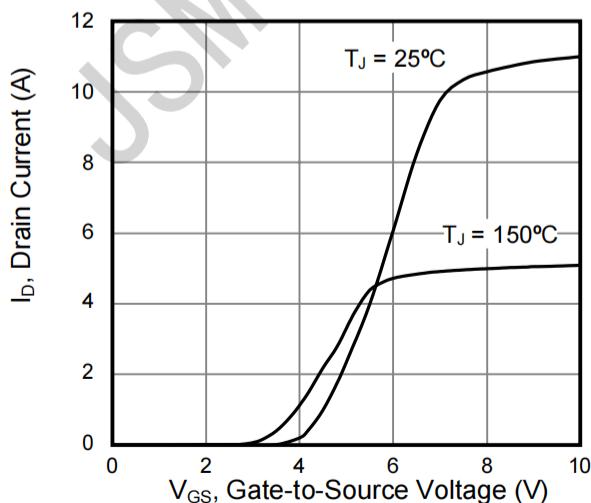
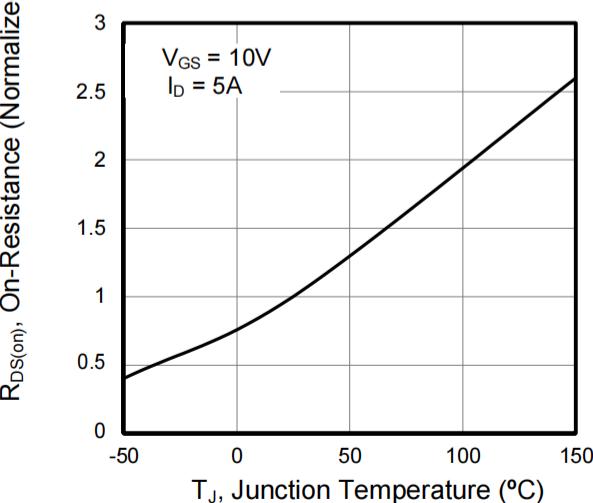
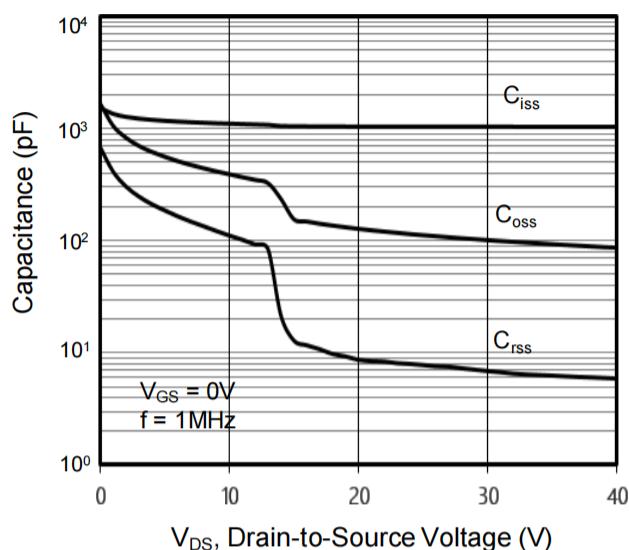


Figure 6. On-Resistance vs. Temperature

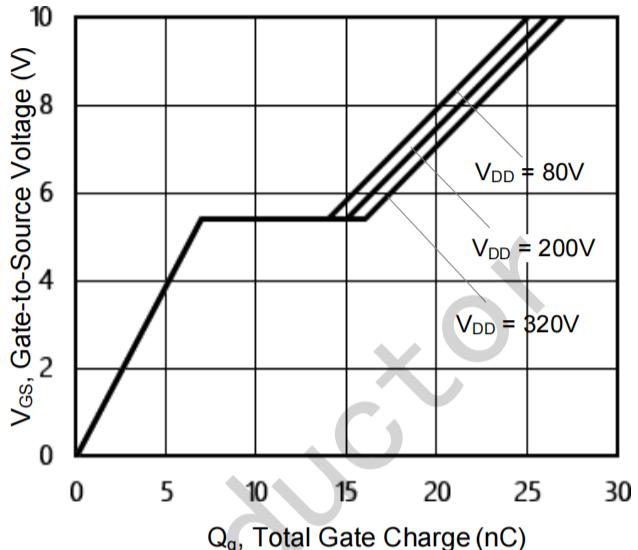


Typical Characteristics  $T_J = 25^\circ\text{C}$ , unless otherwise noted

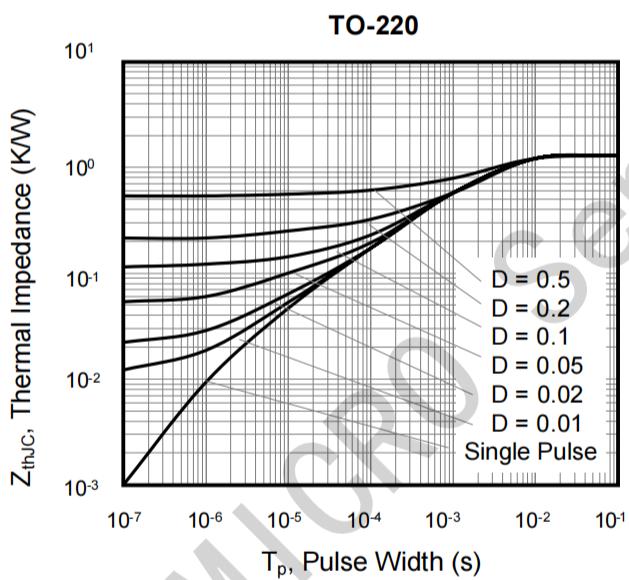
**Figure 7. Capacitance**



**Figure 8. Gate Charge**



**Figure 9. Transient Thermal Impedance**



**Figure 10. Transient Thermal Impedance**

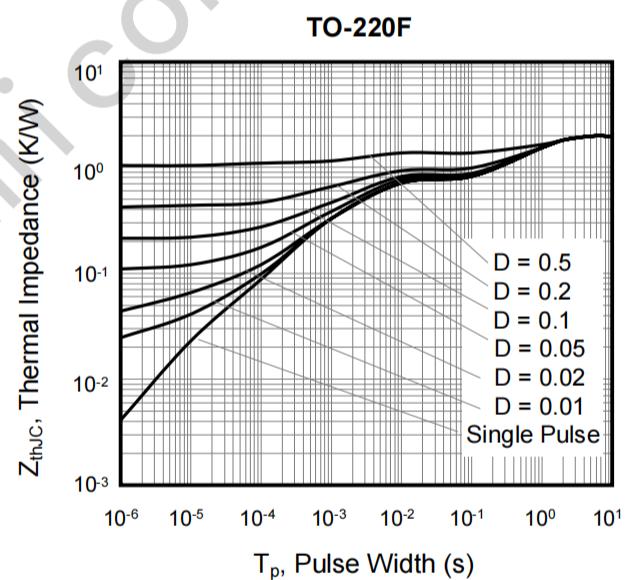


Figure A: Gate Charge Test Circuit and Waveform

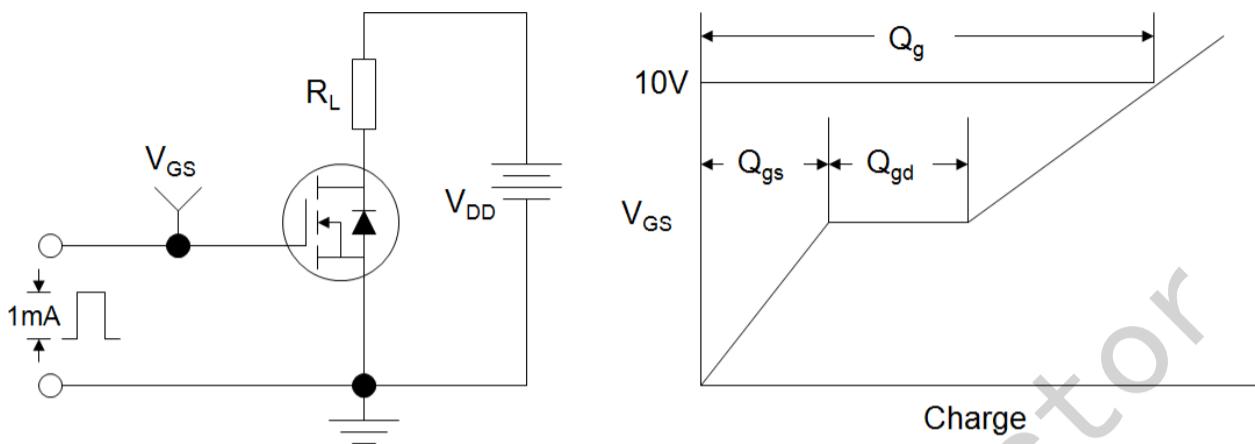


Figure B: Resistive Switching Test Circuit and Waveform

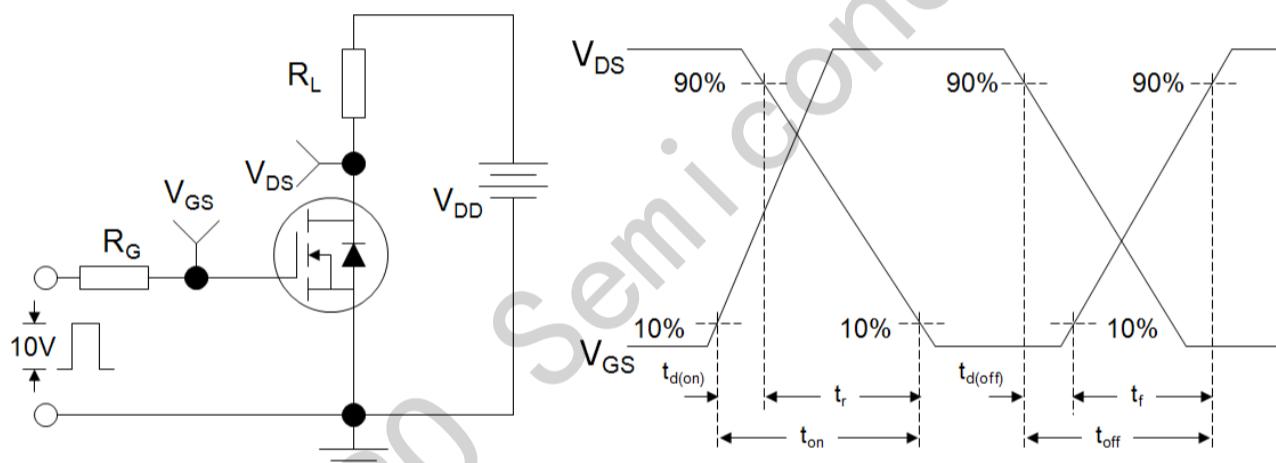
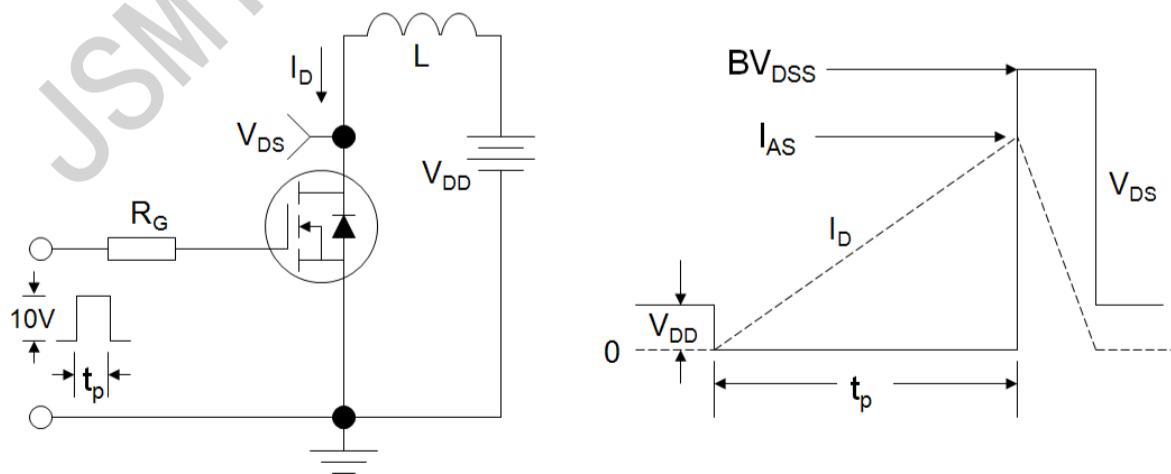
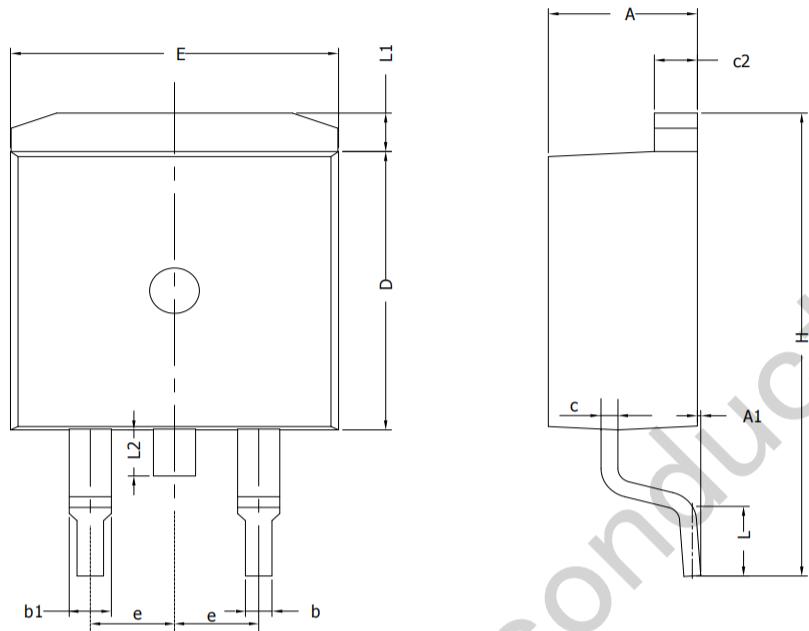


Figure C: Unclamped Inductive Switching Test Circuit and Waveform



## Package Outline: TO-263



SYMBOL	MIN	NOM	MAX
A	4.30	4.57	4.72
A1	0	0.10	0.25
b	0.71	0.81	0.91
c	0.30	---	0.60
c2	1.17	1.27	1.37
D	8.50	---	9.35
E	9.80	---	10.45
e	2.54BSC		
H	14.70	---	15.75
L	2.00	2.30	2.74
L1	1.12	1.27	1.42
L2	---	---	1.75