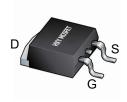


Description

The AOD409 uses advanced trench technology to provide excellent $R_{DS(ON)}$, low gate charge and operation with gate voltages as low as 4.5V. This device is suitable for use as a

Battery protection or in other Switching application.

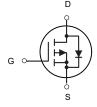


TO252-2L

General Features

 $V_{DS} = -60V I_{D} = -50A$

 $R_{DS(ON)}$ < 24 m Ω @ V_{GS} =10V



P-Channel MOSFET

Application

Battery protection

Load switch

Uninterruptible power supply

Package Marking and Ordering Information

Product ID	Pack	Marking	Qty(PCS)
AOD409	TO252-2L	50P06 XXYY	2500

Absolute Maximum Ratings (T_c=25[°]Cunless otherwise noted)

Symbol	Parameter	Rating	Units		
VDS	Drain-Source Voltage	tage -60			
VGS	Gate-Source Voltage	Source Voltage ±20			
I _D @T _C =25°C	Continuous Drain Current, V _{GS} @ 10V ¹	Prain Current, V _{GS} @ 10V ¹ -50			
I _D @T _C =100°C	Continuous Drain Current, V _{GS} @ 10V ¹	Drain Current, V _{GS} @ 10V ¹ -27			
IDM	Pulsed Drain Current ²	ulsed Drain Current ² -70			
P _D @T _C =25°C	Total Power Dissipation ⁴	pation ⁴ 52.1			
TSTG	Storage Temperature Range	-55 to 150			
TJ	Operating Junction Temperature Range -55 to 150		°C		
R₀JA	Thermal Resistance Junction-ambient ¹	nt ¹ 62			
R₀JC	Thermal Resistance Junction-Case ¹	nermal Resistance Junction-Case ¹ 2.4			



Electrical Characteristics (T_J=25 °C, unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit	
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V , I _D =-250uA	-60		-	>	
$\triangle BV_{DSS} \! / \! \triangle T_J$	BV _{DSS} Temperature Coefficient	Reference to 25°C , I _D =-1mA	1	-0.035	-	V/°C	
D	Static Drain-Source On-Resistance ²	V _{GS} =-10V , I _D =-18A		20	24	mΩ	
R _{DS(ON)}	Static Drain-Source On-Resistance	V _{GS} =-4.5V , I _D =-12A		25	30		
$V_{GS(th)}$	Gate Threshold Voltage	V _{GS} =V _{DS} . In =-250uA	-1.0		-2.5	V	
$\triangle V_{GS(th)}$	V _{GS(th)} Temperature Coefficient	VGS-VDS , ID250UA		4.28		mV/°C	
1	Dunin Course Lookens Courset	V _{DS} =-48V , V _{GS} =0V , T _J =25°C			1		
I _{DSS}	Drain-Source Leakage Current	V_{DS} =-48V , V_{GS} =0V , T_{J} =55 $^{\circ}$ C			5	uA	
I _{GSS}	Gate-Source Leakage Current	$V_{GS}=\pm 20V$, $V_{DS}=0V$			±100	nA	
gfs	Forward Transconductance	V _{DS} =-10V , I _D =-18A		23		S	
R_g	Gate Resistance	V _{DS} =0V , V _{GS} =0V , f=1MHz		7		Ω	
Qg	Total Gate Charge (-4.5V)			25			
Q_{gs}	Gate-Source Charge	V_{DS} =-20V , V_{GS} =-4.5V , I_{D} =-12A		6.7		nC	
Q_{gd}	Gate-Drain Charge			5.5			
T _{d(on)}	Turn-On Delay Time			38			
T _r	Rise Time	V_{DD} =-15V , V_{GS} =-10V , R_{G} =3.3 Ω ,		23.6		20	
$T_{d(off)}$	Turn-Off Delay Time	I _D =-1A		100		ns	
T _f	Fall Time			6.8			
C _{iss}	Input Capacitance			3635			
C _{oss}	Output Capacitance	V _{DS} =-15V , V _{GS} =0V , f=1MHz		224		pF	
C _{rss}	Reverse Transfer Capacitance			141			

Diode Characteristics

Symb	bol	Parameter Conditions		Min.	Тур.	Max.	Unit
Is		Continuous Source Current ^{1,5}	\/ =\/ =0\/ Force Current			-45	Α
I _{SM}	1	Pulsed Source Current ^{2,5}	V _G =V _D =0V , Force Current			-70	Α
V _{SE})	Diode Forward Voltage ²	V _{GS} =0V , I _S =-1A , T _J =25°C	-		-1	V

Note

- 1.The data tested by surface mounted on a 1 inch² FR-4 board with 2OZ copper.
- 2.The data tested by pulsed , pulse width \leq 300us , duty cycle \leq 2%
- 3. The EAS data shows Max. rating . The test condition is V_{DD} =-25V, V_{GS} =-10V, L=0.1mH, I_{AS} =-47.6A
- 4. The power dissipation is limited by 150°C junction temperature
- 5. The data is theoretically the same as I_D and I_{DM} , in real applications, should be limited by total power dissipation.



Typical Characteristics

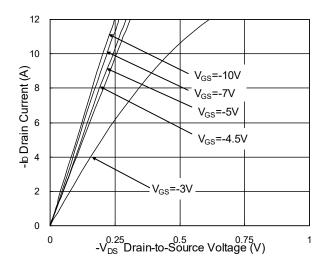


Fig.1 Typical Output Characteristics

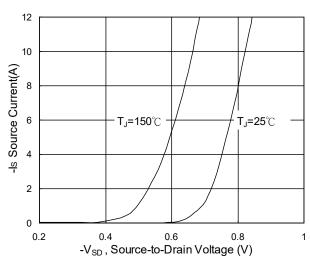


Fig.3 Forward Characteristics Of Reverse

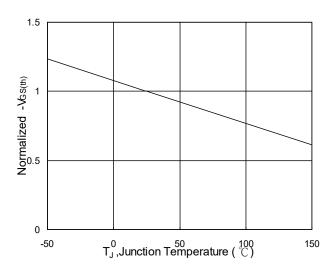


Fig.5 Normalized $V_{\text{GS(th)}}$ v.s T_{J}

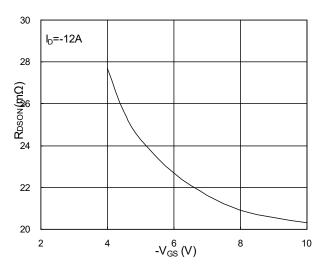


Fig.2 On-Resistance v.s Gate-Source

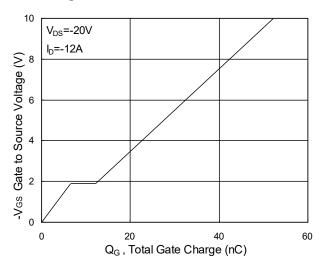


Fig.4 Gate-Charge Characteristics

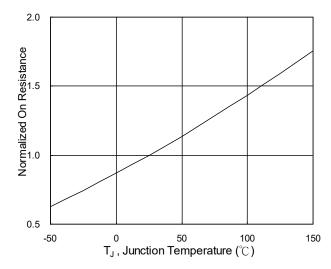
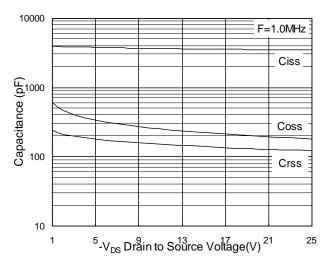


Fig.6 Normalized R_{DSON} v.s T_J



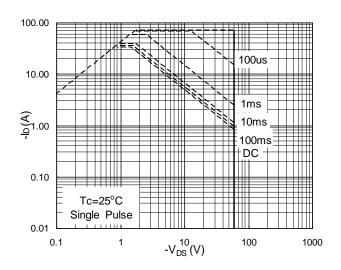
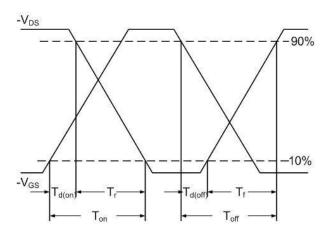


Fig.7 Capacitance Fig.8 Safe Operating Area Normalized Thermal Response (Reuc) DUTY=0.5 0.3 0.1 0.05 0.02 0.01 SINGLE PULSE $T_J peak = T_C + P_{DM} x R_{\theta JC}$ 0.01 0.00001 0.0001 0.001 0.01 t, Pulse Width (s)

Fig.9 Normalized Maximum Transient Thermal Impedance



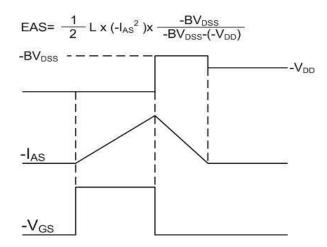
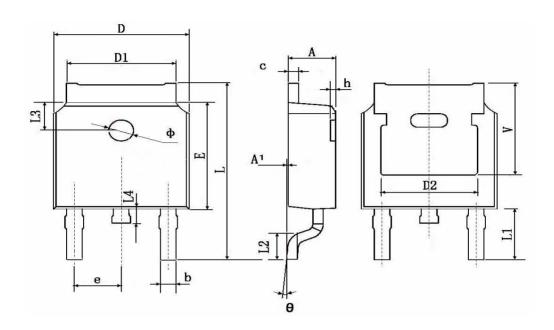


Fig.11 Unclamped Inductive Waveform



TO252-2L Package Information



O mark al	Dimensions In Millimeters		Dimensions In Inches		
Symbol	Min.	Max.	Min.	Max.	
А	2.200	2.400	0.087	0.094	
A1	0.000	0.127	0.000	0.005	
b	0.660	0.860	0.026	0.034	
С	0.460	0.580	0.018	0.023	
D	6.500	6.700	0.256	0.264	
D1	5.100	5.460	0.201	0.215	
D2	4.830 TYP.		0.190 TYP.		
E	6.000	6.200	0.236	0.244	
е	2.186	2.386	0.086	0.094	
L	9.800	10.400	0.386	0.409	
L1	2.900	TYP.	0.114 TYP.		
L2	1.400	1.700	0.055	0.067	
L3	1.600 TYP.		0.063 TYP.		
L4	0.600	1.000	0.024	0.039	
Ф	1.100	1.300	0.043	0.051	
θ	0°	8°	0°	8°	
h	0.000	0.300	0.000	0.012	
V	5.350	TYP.	0.211 TYP.		

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